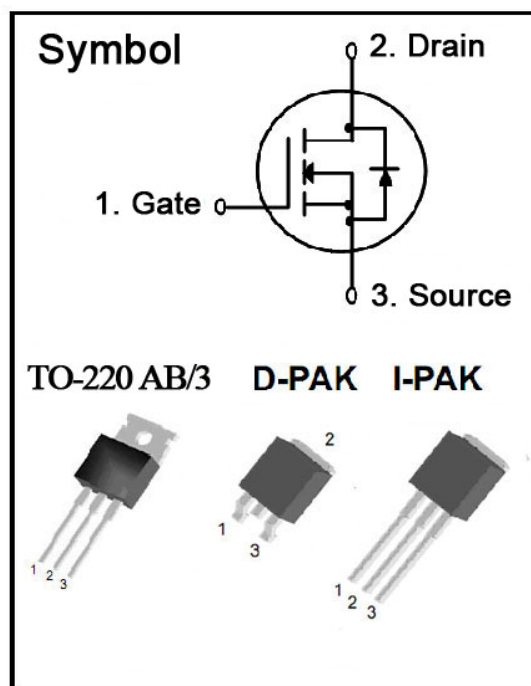


## N-Channel MOSFET

### Features

- $R_{DS(on)}$  (Max 5.0  $\Omega$ )@ $V_{GS}=10$  V
- Gate Charge (Typical 9.5 nC)
- Maximum Junction Temperature Range (150  $^{\circ}$ C)



### Absolute Maximum Ratings

Symbol	Parameter	Value	Units
$V_{DSS}$	Drain to Source Voltage	600	V
$I_D$	Continuous Drain Current(@ $T_C = 25$ $^{\circ}$ C)	1.8	A
	Continuous Drain Current(@ $T_C = 100$ $^{\circ}$ C)	1.1	A
$I_{DM}$	Drain Current Pulsed	6.0 <sup>1)</sup>	A
$V_{GS}$	Gate to Source Voltage	$\pm 30$	V
$E_{AS}$	Single Pulsed Avalanche Energy	120 <sup>2)</sup>	mJ
$E_{AR}$	Repetitive Avalanche Energy	4.4 <sup>1)</sup>	mJ
dv/dt	Peak Diode Recovery dv/dt	4.5 <sup>3)</sup>	V/ns
$P_D$	Total Power Dissipation(@ $T_C = 25$ $^{\circ}$ C)	44	W
	Derating Factor above 25 $^{\circ}$ C	0.35	W/ $^{\circ}$ C
$T_{STG}$	Operating Junction Temperature	-55 ~ 150	$^{\circ}$ C
$T_J$	Storage Temperature	150	$^{\circ}$ C

### Notes

- 1).. Repeativity rating : pulse width limited by junction temperature
- 2).. L = 68 mH,  $I_{AS} = 1.8$  A,  $V_{DD} = 50$  V,  $R_G = 25$   $\Omega$  , Starting  $T_J = 25$   $^{\circ}$ C
- 3)..  $I_{SD} \leq 2.0$  A, di/dt  $\leq 200$  A/us,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25$   $^{\circ}$ C

## Thermal Characteristics

Symbol	Parameter	Value			Units
		Min.	Typ.	Max.	
R <sub>θJC</sub>	Thermal Resistance, Junction-to-Case	-	-	0.85 <sup>1)</sup>	°C/W
		-	-	2.87	
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	-	0.5 <sup>1)</sup>	-	°C/W
		-	-	50	
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	-	-	62.5 <sup>1)</sup>	°C/W
		-	-	110	

<sup>1)</sup>.. For package TO-220 AB/3

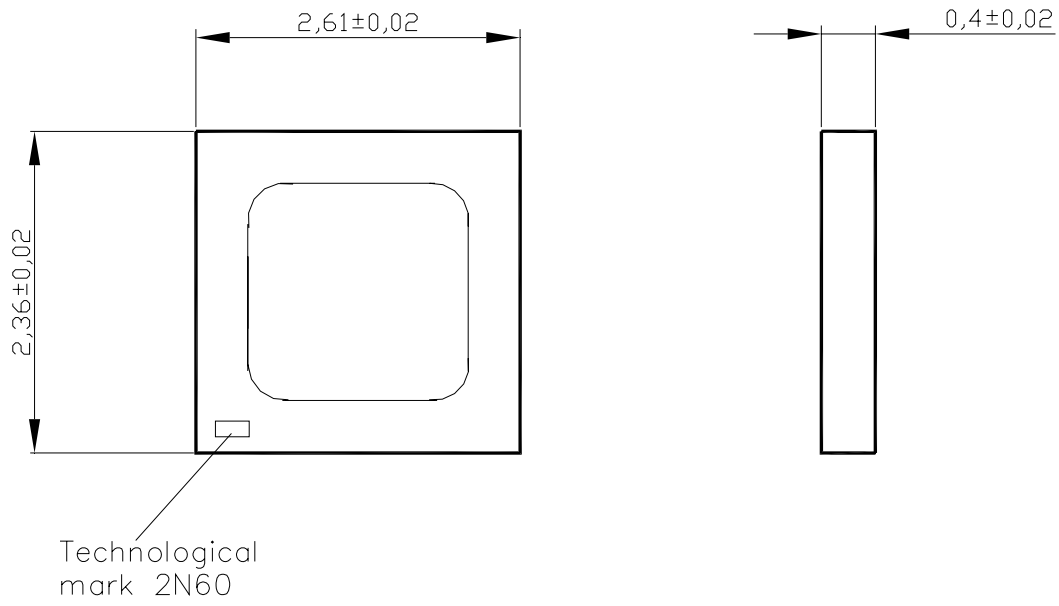
## Source-Drain Diode Characteristics and Maximum Ratings

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
I <sub>S</sub>	Maximum Continuous Source-Drain Diode Forward Current		-	-	1.8	A
I <sub>SM</sub>	Maximum Pulsed Source-Drain Diode Forward Current		-	-	6.0	
V <sub>SD</sub>	Diode Forward Voltage	I <sub>S</sub> = 1.8 A, V <sub>GS</sub> = 0 V	-	-	1.4	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>S</sub> = 2.0 A, V <sub>GS</sub> = 0 V, dI <sub>F</sub> /dt = 100 A/us	-	230	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge	t <sub>i</sub> ≤ 300 us; Q > 50	-	1.0	-	uC

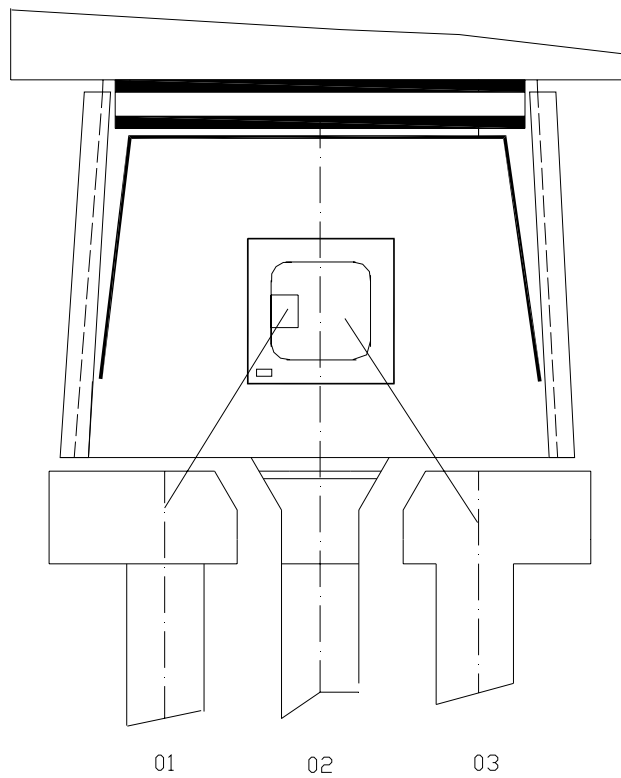
Electrical Characteristics (  $T_C = 25\text{ }^\circ\text{C}$  unless otherwise noted )

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}$ , $I_D = 250\text{ }\mu\text{A}$	600	-	-	V
$\Delta BV_{DSS}/\Delta T_J$	Breakdown Voltage Temperature coefficient	$I_D = 250\text{ }\mu\text{A}$ , referenced to $25\text{ }^\circ\text{C}$	-	0.6	-	V/ $^\circ\text{C}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS} = 600\text{ V}$ , $V_{GS} = 0\text{ V}$	-	-	10	$\mu\text{A}$
		$V_{DS} = 480\text{ V}$ , $T_C = 125\text{ }^\circ\text{C}$	-	-	100	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage, Forward	$V_{GS} = 30\text{ V}$ , $V_{DS} = 0\text{ V}$	-	-	100	nA
	Gate-source Leakage, Reverse	$V_{GS} = -30\text{ V}$ , $V_{DS} = 0\text{ V}$	-	-	-100	nA
<b>On Characteristics</b>						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$ , $I_D = 250\text{ }\mu\text{A}$	2.0	-	4.0	V
$R_{DS(ON)}$	Static Drain-Source On-state Resistance	$V_{GS} = 10\text{ V}$ , $I_D = 0.9\text{ A}$	-	4.0	5.0	$\Omega$
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{GS} = 0\text{ V}$ , $V_{DS} = 25\text{ V}$ , $f = 1\text{ MHz}$	-	320	420	pF
$C_{oss}$	Output Capacitance		-	35	46	
$C_{rss}$	Reverse Transfer Capacitance		-	4.5	6.0	
<b>Dynamic Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD} = 300\text{ V}$ , $I_D = 2.0\text{ A}$ , $R_G = 25\text{ }\Omega$ Pulse Width $\leq 300\mu\text{s}$ , $Q > 50$	-	8	30	ns
$t_r$	Rise Time		-	23	60	
$t_{d(off)}$	Turn-off Delay Time		-	25	60	
$t_f$	Fall Time		-	28	70	
$Q_g$	Total Gate Charge	$V_{DS} = 480\text{ V}$ , $V_{GS} = 10\text{ V}$ , $I_D = 2.0\text{ A}$	-	9.5	13	nC
$Q_{gs}$	Gate-Source Charge		-	1.6	-	
$Q_{gd}$	Gate-Drain Charge (Miller Charge)		-	4.0	-	

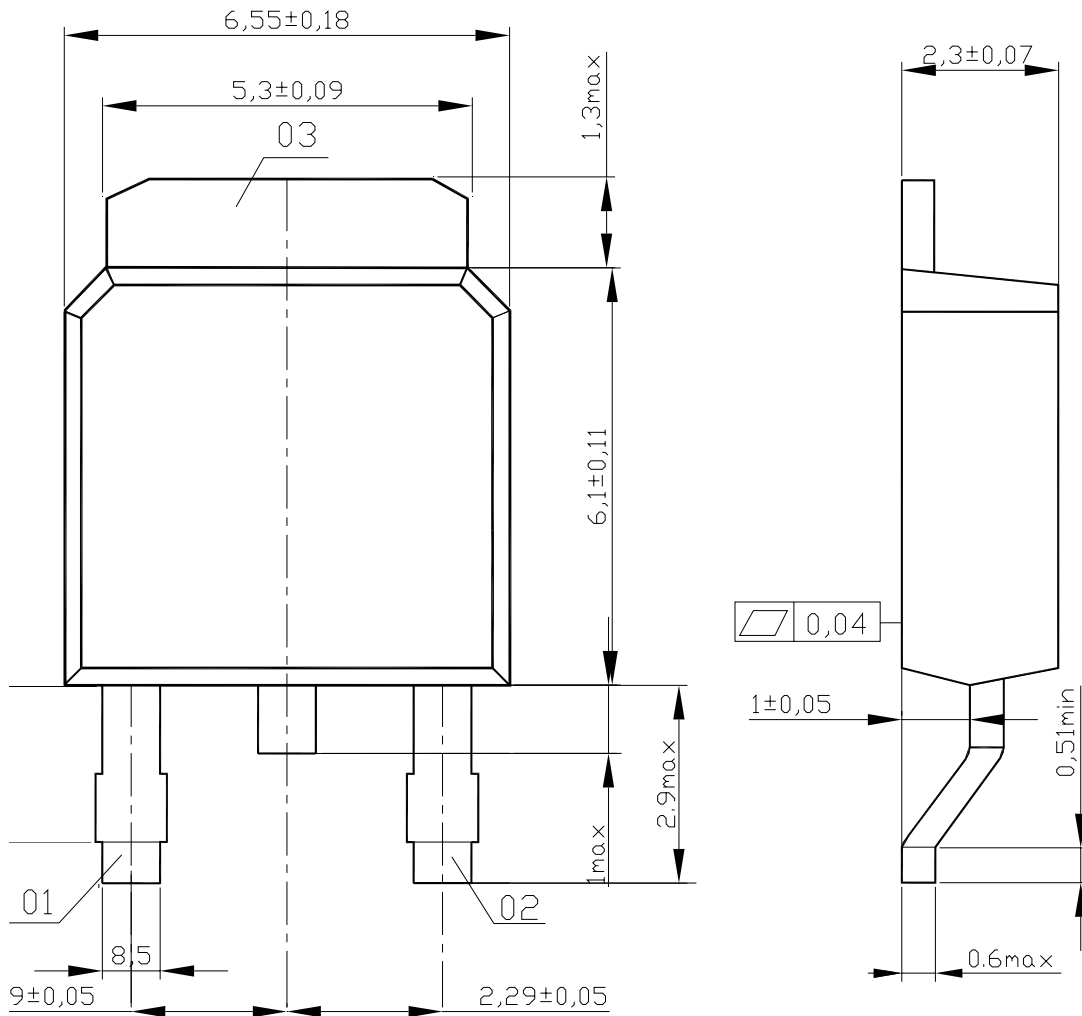
**Chip size**



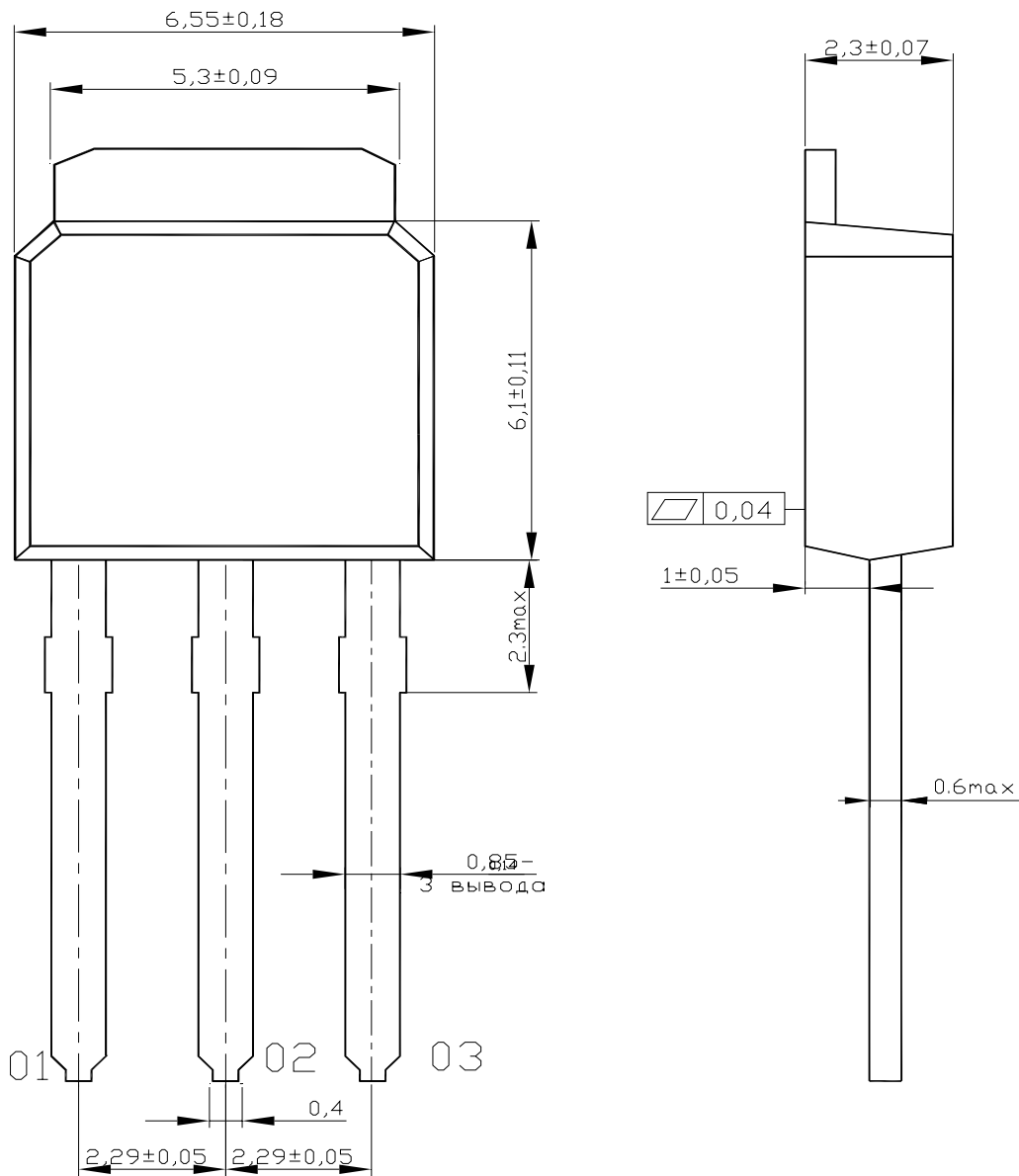
**Package Chip**



Package Dimensions D-PAK



Package Dimensions I-PAK



Package Dimensions TO-220 AB/3

